

**APPARATUS AND METHOD FOR REDUCING FALSE TRIGGERING
OF A SIGNAL DUE TO AN ELECTROSTATIC DISCHARGE EVENT**

ABSTRACT OF THE DISCLOSURE

An apparatus and method for reducing false triggering of a signal due to an electrostatic discharge event are disclosed. The method includes detecting a
5 high voltage on a signal received at an input of a delay circuit and delaying the signal between the input of the delay circuit and an output of the delay circuit for a predetermined amount of time. If a low voltage is detected on the signal after the predetermined amount of
10 time, the high voltage is prevented from propagating to the output of the delay circuit.

BEST AVAILABLE COPY